

L Number	Hits	Search Text	DB	Time stamp
1	31	(network adj processor) same fpga same asic same standard	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/02 15:17
2	1	(logic adj file) same (verification adj module)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/02 15:20
3	1	(logic adj file) and (verification adj module)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/02 15:21
4	582	verification adj module	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/02 15:21
5	1	((network adj processor) same fpga same asic same standard) and (verification adj module)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/02 15:21
6	227	logic adj file	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/02 15:21
7	1	((network adj processor) same fpga same asic same standard) and (logic adj file)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/02 15:33
8	5147	asic same fpga	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/02 15:35
9	32091	(network adj processor) or np	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/02 15:37
10	180	(asic same fpga) and ((network adj processor) or np)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/02 15:37
11	1	(logic adj file) and ((asic same fpga) and ((network adj processor) or np))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/02 15:37
12	2	(verification adj module) and ((asic same fpga) and ((network adj processor) or np))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/02 15:41
13	1	vendor and ((verification adj module) and ((asic same fpga) and ((network adj processor) or np)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/02 15:41
-	2883	network adj processor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/05 09:10

-	5	(network adj processor) same fpga same asic same standard	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/02/02 14:50
-	5546	standard adj cell	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/05 07:28
-	2	((network adj processor) same fpga) and (standard adj cell)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/05 07:27
-	15306	standard adj3 cell	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/05 07:29
-	326	standard adj5 asic	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/05 07:29
-	1	((network adj processor) same fpga) and (standard adj5 asic)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/05 07:30
-	9477	fpga	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/05 07:30
-	71	(standard adj5 asic) and fpga	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/05 07:30
-	15	(network adj processor) and ((standard adj5 asic) and fpga)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/05 07:49
-	3533	asic same fpga	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/05 07:49
-	73	(network adj processor) and (asic same fpga)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/05 07:49
-	1080	common adj logic	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/05 07:50
-	1	((network adj processor) and (asic same fpga)) and (common adj logic)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/05 07:50
-	41	(network adj processor) same fpga	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/05 08:37

-	163	custom\$5 same (network adj processor)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/05 08:55
-	50	fpga and (custom\$5 same (network adj processor))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/05 08:55
-	203	(network adj processor) same architecture	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/05 09:11
-	30	fpga and ((network adj processor) same architecture)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/05 09:11